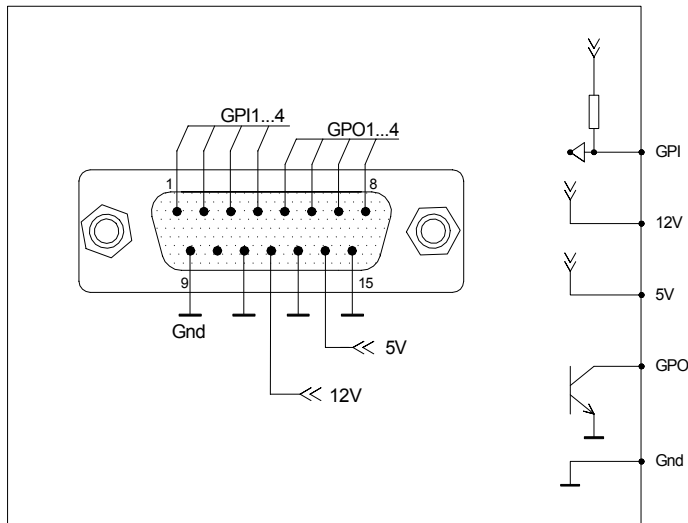


## IMPORTANT

Before using the control ports specified in this drawing, read the associated chapters in the user manual. Improper use may lead to damage of your VIPdigital and/or the equipment connected to the GPIs/GPOs.

All examples imply the connection of external voltages: e.g. 5V, 12V or 24V or 0V (Ground). Provided that you consider the restrictions described in the user manual, you may as well use the auxiliary voltages or the Ground Pin of your VIPdigital.

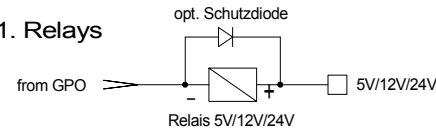
Control Input Ports: GPI 1...4  
Control Output Ports: GPO 1...4



Diese Zeichnung ist unser Eigentum. Jede Vervielfältigung oder Mitteilung ist strafbar und kann gerichtlich verfolgt werden. (Urheberrechtsgesetz, Gesetz gegen unlauteren Wettbewerb, BGB) Technische Änderungen vorbehalten.

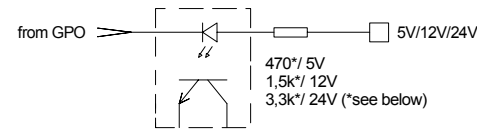
## Examples for GPO - Connections

### 1. Relays



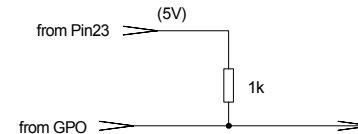
GPO active: Relais triggered

### 2. Optocouplers



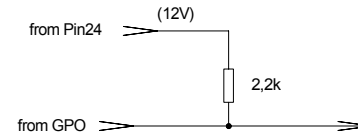
GPO active: Optocoupler triggered

### 3. TTL - Logic



GPO active: <1V  
GPO not active: >2V

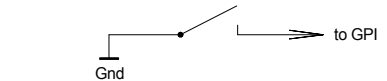
### 4. CMOS - Logic



GPO active: <1V  
GPO not active: >3,5V

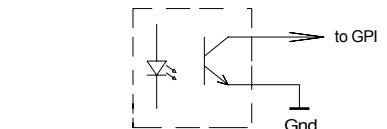
## Examples for GPI - Connections

### 1. Switches/Relays



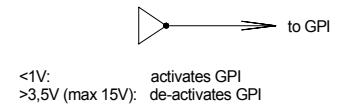
Switch on: activates GPI

### 2. Optocouplers



Optocoupler triggered: activates GPI

### 3. External Logic



\*The resistor-values are suitable for commonly used optocouplers. It is essential to check whether they match for the type you are using.

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Connection of VIPdigital GPI and GPO

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Datei: VIPd\_GPIO\_en 8